

```
    load'cpul.ijs'
    mux
3 : 0
'selector inputs' =. y.
selector from inputs
)
    inputs =: 0 1 1 0
    mux 1 ; inputs
1
    link
;
    1, inputs
1 0 1 1 0
    1;inputs
++-----+
|1|0 1 1 0|
++-----+
    dtb
|value error: dtb
    require'circuits.ijs'
    dtb
|value error: dtb
    require'16-bit-adder.ijs'
    dtb
(16$2)&#:
    btd
signed_value
    mux (btd 0 1);inputs
1
```